

PATENT APPLICATION
Attorney Do. No. 5038-318
Intel No. P11250

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: David G. Chow

Confirmation No.: 7112

Serial No.: 10/655,810 Examiner: Trong Q. Phan

Filing Date: September 4, 2003 Group Art Unit: 2818

For: FERROELECTRIC MEMORY INPUT/OUTPUT APPARATUS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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A DOUBLE PATENTING REJECTION**

I, Julie L. Reed, patent counsel for Intel Corporation, having a place of business at 2200 Mission College Blvd., Santa Clara, CA 95052, assignee of the entire right, title and interest of the above described U.S. Patent Application No. 10/655,810 filed on September 4, 2003, for FERROELECTRIC MEMORY INPUT/OUTPUT APPARATUS, by assignment recorded in Reel/Frame 012361/0977 (4 pages) on December 3, 2001 (for priority U.S. Patent Application No. 10/005,676 filed on December 3, 2001), represent that I am authorized to sign on behalf of assignee.

I hereby disclaim the terminal part of any patent granted on U.S. Patent Application No. 10/655,810, which would extend beyond the expiration of U.S. Patent No. 6,646,903, that is December 3, 2021, and hereby agree that any patent so granted on the above-identified application shall be enforceable only for and during such period that the legal title to said patent shall be the same as the legal title to U.S. Patent Application No. 10/055,676, this

agreement to run with any patent granted on the above-identified application and to be binding upon the grantee, its successors or assigns.

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FROM: Julie Reed, Registration No. 35,349

Date: June 21, 2004

Re: **Attorney Do. No. 5038-318 – TERMINAL DISCLAIMER and
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REMARKS:

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Enclosed for filing in the above-referenced application are the following:

- ☒ Terminal Disclaimer to Obviate a Double Patenting Rejection and
- ☒ PTO Form 2038 authorizing credit card payment for the required fee.
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Respectfully submitted,

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